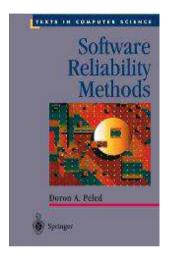
VERIFICA DI PROCESSI CONCORRENTI 19-20

Analysis: model checking LTL

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Reference material books:



Concepts, Algorithms, and Tools for Model Checking

Joost-Pieter Katoen Lehrstuhl für Informatik VII Friedrich-Alexander Universität Erlangen-Nürnberg

Lecture Notes of the Course "Mechanised Validation of Parallel Systems" (course number 10359) Semester 1998/1999

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http://www.dcs.warwick.ac.uk/~doron/srm.html

- Prof. Jost-Pieter Katoen, University of Aachen (Germany)
- Dr. Jeremy Sproston, Universita' di Torino (Italy)



Concentrate on distributed systems (as inherently protocols are)

Learn several formalisms to model system and properties (automata, process algebras, Petri Nets, temporal logic, timed automata).

Learn advantages and limitations, in order to choose the right methods and tools.

Learn how to combine existing formalisms and existing "solution" methods.

Flowchart of analysis material

- 1. Basic properties
- 2. RG analysis
- 3. Structural analysis (on PN)
- 4. Reduction rules (PN)
- 5. Equivalences (PA)
- 6. Model checking
 - definition of linear logic LTL and its model checking algorithm
 - definition of branching logic CTL and its model checking algorithm

Some important points

- Reachable states: obtained from an initial state through a sequence of enabled transitions.
- Executions: the set of maximal paths (finite or terminating in a node where nothing is enabled).
- Nondeterministic choice: when more than a single transition is enabled at a given state. We have a nondeterministic choice when at least one node at the state graph has more than one successor.

useful:The interleaving model

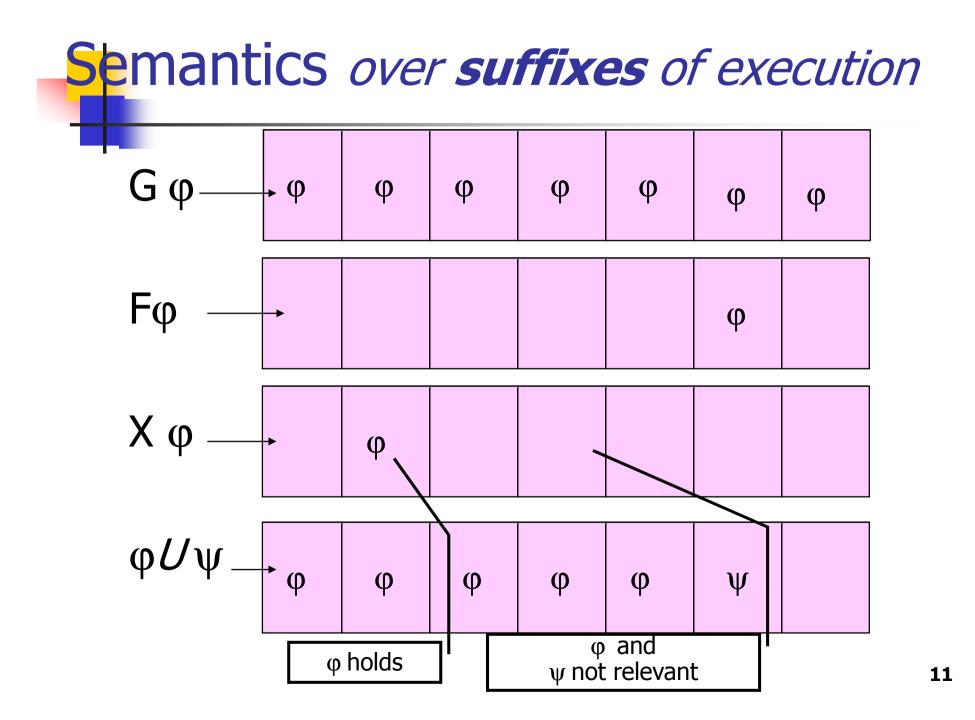
- An execution is a finite or infinite sequence of states s₀, s₁, s₂, ...
- The initial state satisfies the initial condition, I.e., $I(s_0)$.
- Moving from one state s_i to s_{i+1} is by executing a transition
 e→t:
 - e(s_i), I.e., s_i satisfies e.
 - s_{i+1} is obtained by applying t to s_i .
- Lets assume all sequences are infinite by extending finite ones by "stuttering" the last state.



- A (finite) set of variables V.
- A set of states Σ .
- A (finite) set of transitions T, each transition e→t has
 - an enabling condition e and a transformation t.
- An initial condition *I*.
- Denote by R(s, s') the fact that s' is a successor of s.

Linear temporal logic (LTL)

- LTL has been introduced by Pnueli in 1977
- It is a logic to describe systems in terms of linear executions: total order between events
- Interpretation: over an execution, later over all executions.
- LTL is very popular in industry mainly thanks to the LTL model checker SPIN (by Holzmann et al. in the 90's)



Can discard some operators

- Instead of F*p*, write *true U p*.
- Instead of G*p*, we can write $\neg(F\neg p)$, or $\neg(true U \neg p)$.

Because $Gp = \neg \neg Gp$.

 $\neg Gp$ means it is not true that p holds forever, or at some point $\neg p$ holds or $F\neg p$.



• $(GF_p) \rightarrow (GF_q)$ "If *p* happens infinitely often, then *q* also happens infinitely often".

Formal semantic definition -Peled's book

Let σ be a sequence $s_0 s_1 s_2 \dots$

- Let σ^i be a suffix of σ : $s_i \ s_{i+1} \ s_{i+2} \ \dots \ (\sigma^0 = \sigma)$
- $\sigma^i \mid = p$, where p is a proposition, if $s_i \mid = p$.

•
$$\sigma^i \mid = \phi / \psi$$
 if $\sigma^i \mid = \phi$ and $\sigma^i \mid = \psi$.

•
$$\sigma^i \models \phi \lor \psi$$
 if $\sigma^i \models \phi$ or $\sigma^i \models \psi$.

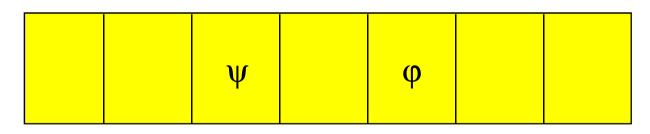
•
$$\sigma^i \mid = \neg \phi$$
 if it is not the case that $\sigma^i \mid = \phi$.

•
$$\sigma^i \models X\phi$$
 if $\sigma^{i+1} \models \phi$.

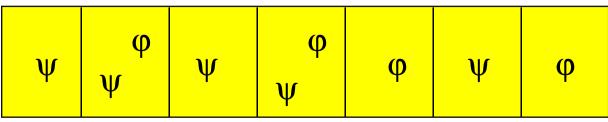
- $\sigma^i \models F\phi$ if for some $j \ge i$, $\sigma^j \models \phi$.
- $\sigma^i \models G\phi$ if for each $j \ge i$, $\sigma^j \models \phi$.
- $\sigma^i \mid = \varphi U \psi$ if for some $j \ge i$, $\sigma^j \mid = \psi$. and for each $i \le k < j$, $\sigma^k \mid = \varphi$.



- $G(\phi/\psi)=(G\phi)/(G\psi)$
- But F(φ/\ψ)≠(Fφ)/\(Fψ)

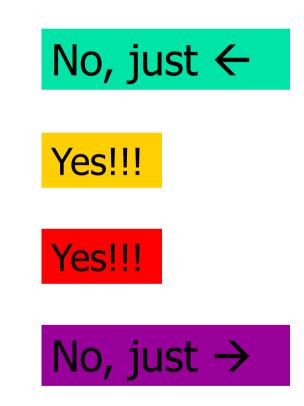


- $F(\phi \lor \psi) = (F\phi) \lor (F\psi)$
- But G(φ\/ψ)≠(Gφ)\/(Gψ)



- $(FG\phi)/(FG\psi)=FG(\phi/\psi)?$
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Formal semantic definition -Peled's book

LTL formulas are interpreted over a linear model: infinite sequences over S

Given a sequence σ and a formula φ , we define the satisfaction relation |=, as $(\sigma, \varphi) \in |=$, and we write $\sigma \mid = \varphi$.

Formal semantic definition -Peled's book

Let σ be a sequence $s_0 s_1 s_2 \dots$

- Let σ^i be a suffix of σ : $s_i \ s_{i+1} \ s_{i+2} \ \dots \ (\sigma^0 = \sigma)$
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- $\sigma^i \mid = \varphi U \psi$ if for some $j \ge i$, $\sigma^j \mid = \psi$. and for each $i \le k < j$, $\sigma^k \mid = \varphi$.

Formal semantic definition -Katoen's book

LTL formulas are interpreted over a linear model M(S, R, L)

where

S is a set of states

R:S-->S is a successor function (total function), assigning to s its unique successor R(s)

L:S-->2^{AP}, is a labelling function

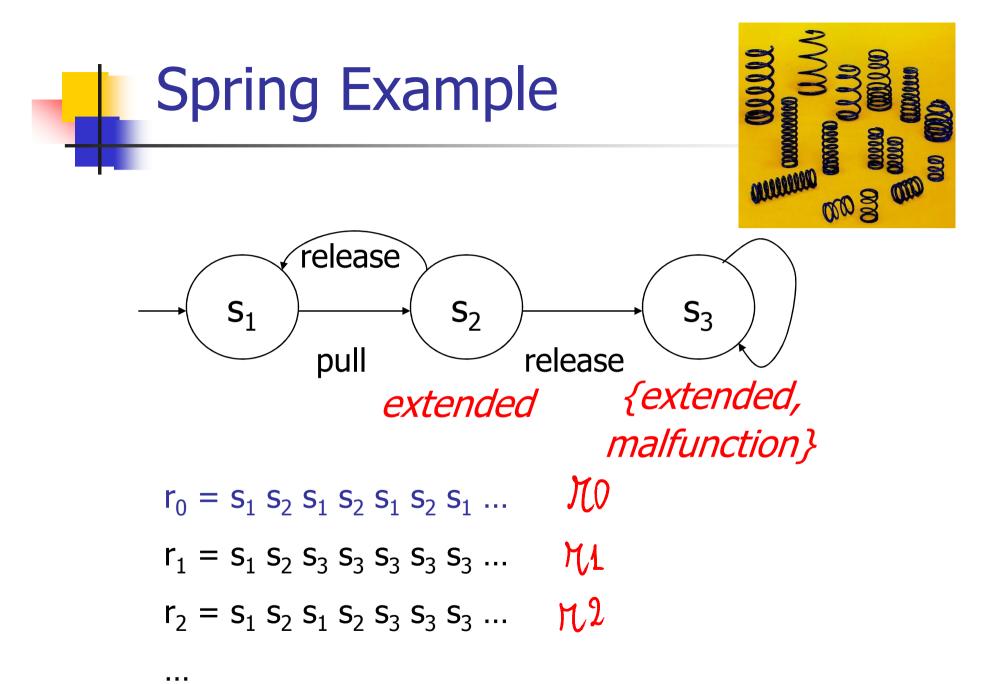
M can be seen as an infinite sequence over S

Given a model M and a formula φ , we define the satisfaction relation as $(M,s,\varphi) \in |=$, and we write $(M,s) |=\varphi$.

Formal semantic definition -Katoen's book

Let $R^{0}(s) = s$ and $R^{n+1}(s) = R(R^{n}(s))$, for any n > 0

s |= p, where p a proposition, if p ∈ L(s).
s |= φ/\ψ if s |= φ and s |= ψ.
s |= φ\/ψ if s |= φ or s |= ψ.
s |= ¬φ if ¬(s |= φ).
s |= Fφ if ∃ j≥0: R^j(s) |= φ.
s |= Kφ if R(s) |= φ.
s |= Gφ if for each j≥0, R^j(s) |= φ.
s |= φUψ if for some j≥0, R^j(s) |=ψ. and for each 0≤k<j, R^k(s) |=φ.



Esempi dal testo di Katoen

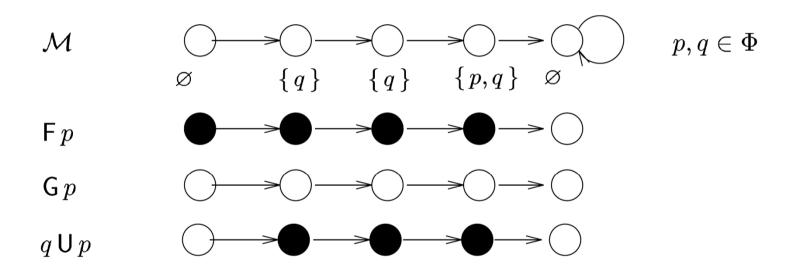
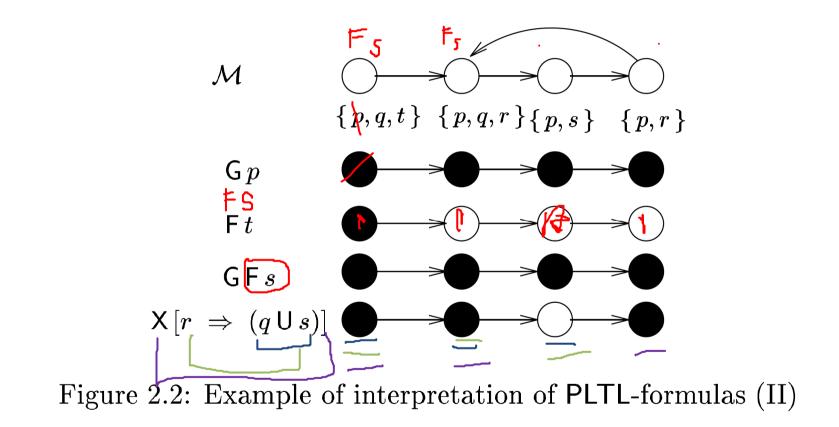


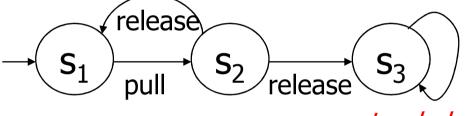
Figure 2.1: Example of interpretation of PLTL-formulas (I)

Esempi dal testo di Katoen





 $\mathbf{r}_2 = \mathbf{s}_1 \, \mathbf{s}_2 \, \mathbf{s}_1 \, \mathbf{s}_2 \, \mathbf{s}_3 \, \mathbf{s}_3 \, \mathbf{s}_3 \dots$



extended

extended malfunction

- $r_2 \mid = extended ??$
- $r_2 \mid = X$ extended ??
- $r_2 \mid = X X$ extended ??
- $r_2 \models F$ extended ??

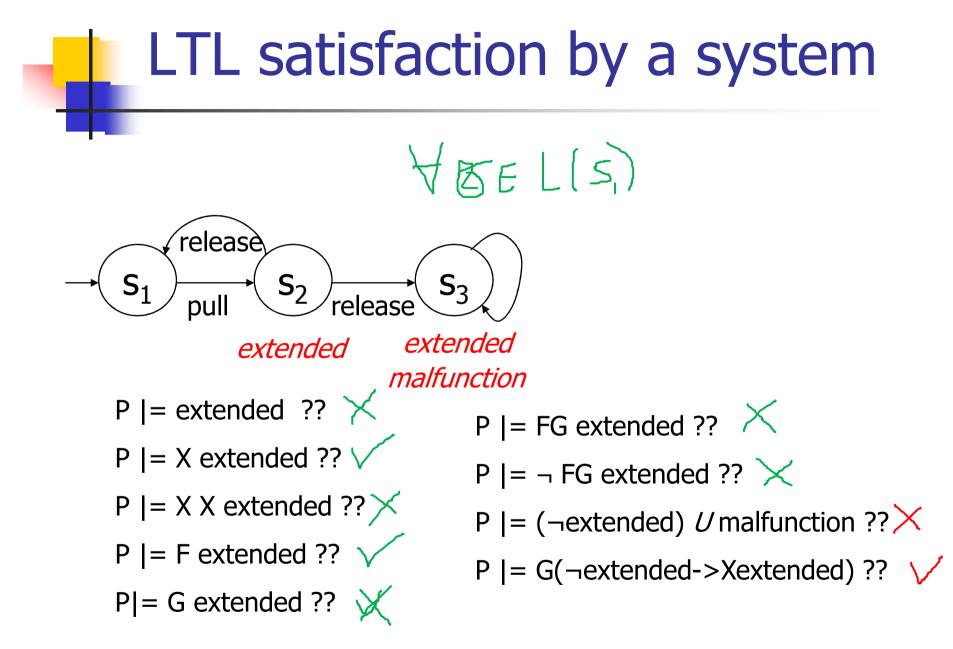
 $r_2 \mid = G$ extended ??

 $r_2 \models FG extended ??$

- $r_2 \mid = \neg FG$ extended ??
- $r_2 \mid = (\neg extended) U malfunction ??$

$$r_2 \models G(\neg extended \rightarrow X extended)$$

 $G(extended \lor X extended)$



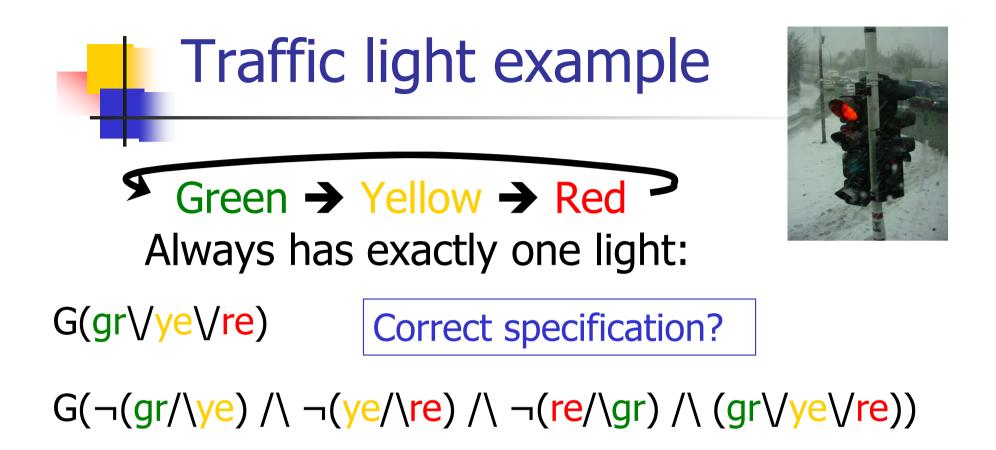
Exercise

Try at home over Dekker's algorithm:

- The processes alternate in entering their critical sections.
- Each process that tries to enter the critical section will eventually be allowed to enter it (responsiveness).

- Each process enters its critical section infinitely often.

- When a process enters its trying section, it will remain there, unless it progresses to its critical section

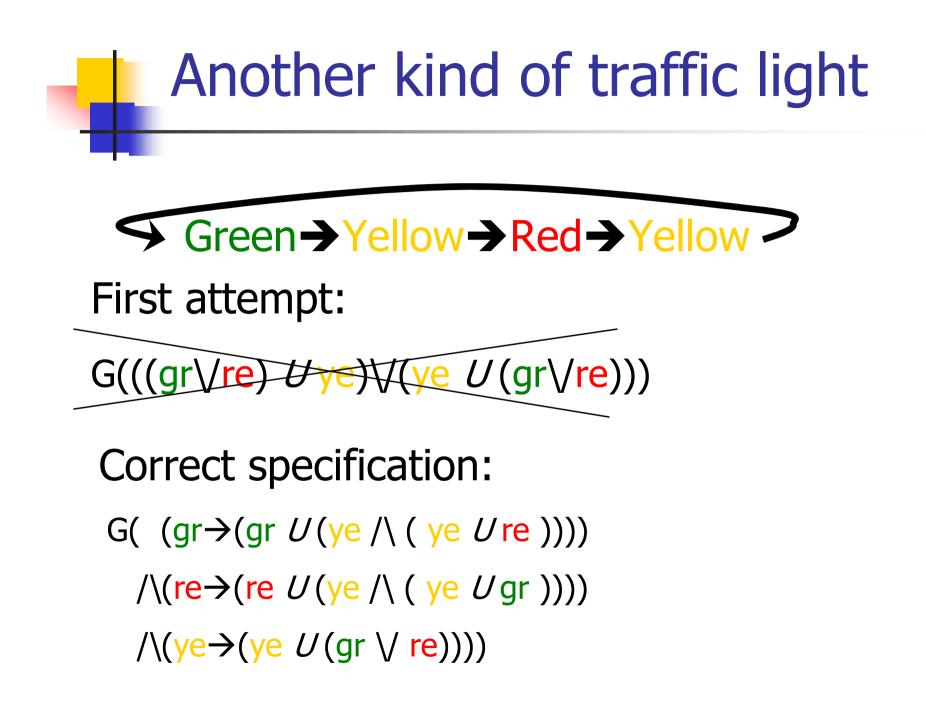


Correct change of color:

G((grUye))/(yeUre))/(reUgr))

Correct specification?

What if colour does not change? 27



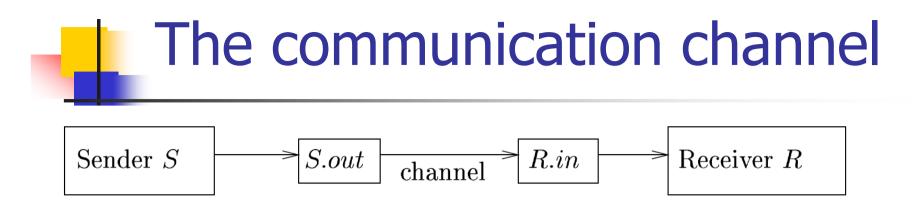
LTL properties and PN

We can specify the traffic light as a (very) simple PN, and then check the previous properties.What is needed: a language for the definition of AP

La specifica del semaforo è equivalente a: G((gr /\ X ye) \lor (ye /\ X re) \lor (re /\ X gr))?



- init-when the program starts and satisfies the initial condition.
- finish-when the program terminates and nothing is enabled.
- q: the correct function has been computed
- Partial correctness: init/\G(finish \rightarrow q)
- Termination: init/\F finish
- Total correctness: init/\F(finish/\ q)
- Invariant: init/\Gp



- Sender S, output buffer S.out, input buffer R.in, Receiver R
- prop1: a message cannot be in both buffers at the same time

$$\mathsf{G} \neg (m \in S.out \land m \in R.in)$$

 prop2: the channel does not loose messages (whatever is in S.out will be in R.in)

$$\mathsf{G} \ (m \in S.out \ \Rightarrow \ \mathsf{F} \ (m \in R.in))$$

The communication channel

Prop 2, cont.: since m can't be in both,

 $\mathsf{G} \ (m \in S.out \ \Rightarrow \ \mathsf{X} \, \mathsf{F} \ (m \in R.in))$

prop3: the channel is order preserving

 $\mathsf{G} \ (m \in S.out \ \land \ \neg m' \in S.out \ \land \ \mathsf{F} \ (m' \in S.out)$

 $\Rightarrow \mathsf{F}(m \in R.in \land \neg m' \in R.in \land \mathsf{F}(m' \in R.in)))$

prop4: the channel does not spontaneously generate messages

$$G (m \in R.in \Rightarrow F^{-1} (m \in S.out))$$

G ((\(\ng m \in R.in\)) U (m \in S.out)) Correct
specification?

Model-Checking LTL

The model-checking problem is: given a (finite) model M, a state s, and a property ψ , do we have s|= ψ ?

It is different from satisfiability: given a formula ψ , does it exists a model and a state s, such that: $(M,s)|=\psi$?

Satisfiability is decidable for LTL --> model-checking is decidable



The validity problem is:

given a property ψ , do we have for all models M, and for all states s in these models,that $(M,s)|=\psi$?

Logically this is equivalent to the satisfiability of $\neg\psi$

Note: Valid formula are the basis for re-writing rules

Model-Checking LTL

Validity can be based on the semantics, or we can use the syntax and a set of proof rules that allows the re-writing, at a syntactical level, of LTL formulas into semantically equivalent LTL formula

Rewriting rules are of the form $\psi = \varphi$, and they need to be valid (*sound*) {for all M and s: $(M,s) = \psi$ iff $(M,s) = \varphi$?

Ex: $GG\phi = G\phi$, or $FGF\phi = GF\phi$

Some sound rules for LTL

| Duality axioms: | $\neg G\Phi$ | \equiv | $F \neg \Phi$ | |
|---------------------|----------------------------|----------|--|---|
| | \neg F Φ | \equiv | $G \neg \Phi$ | |
| | $\neg X \Phi$ | ≡ | $X \neg \Phi$ | |
| Idempotency axioms: | $GG\Phi$ | \equiv | ${\sf G}\Phi$ | |
| | $FF\Phi$ | \equiv | $F\Phi$ | |
| | $\Phi U (\Phi U \Psi)$ | \equiv | Φ U Ψ | |
| | $(\Phi U \Psi) U \Psi$ | ≡ | Φ U Ψ | |
| Absorption axioms: | $FGF\Phi$ | ≡ | $GF\Phi$ | |
| | $GFG\Phi$ | ≡ | FG Φ | |
| Commutation axiom: | ${\sf X}(\Phi{\sf U}\Psi)$ | \equiv | $({\sf X}\Phi){\sf U}({\sf X}\Psi)$ | |
| Expansion axioms: | $\Phi U \Psi$ | ≡ | $\Psi \ \lor \ (\Phi \ \land \ X \left(\Phi U \Psi \right) \right)$ | |
| Used for recursive | FΦ | \equiv | Φ \lor XF Φ | |
| model checking | $G\Phi$ | ≡ | $\Phi \wedge XG\Phi$ | 6 |

Provate con il tool SPOT <u>https://spot.lrde.epita.fr/app/</u> a tradurre formule in Automi di Buchi. Per ogni formula il traduttore produce un automa (visibile in modo grafico) che accetta tutte e sole le sequenze che soddisfano la formula. Sono automi di Buchi, quindi la regola di accettazione non è quella degli automi a stati finiti, ma si definisce che una sequenza infinita è accettata da un automa di Buchi se il cammino di accettazione della sequenza nell'automa passa infinitamente spesso dagli stati accettanti.

Osservate che le coppie di formule della pagina precedente producono lo stesso Automa di Buchi. La sintassi di SPOT usa ! e & per OR e AND (rispettivamente)

Provate anche coppie di formule che volete confrontare. Per esempio potremmo chiederci se $F(p \cup q) \in F(s \cup q)$ sono equivalenti. Lo sono??

Model-Checking LTL

Commonly used formulas:

| pattern | category | PLTL-formula | frequency |
|--|----------------------|---|-----------------|
| response | liveness | $G \left(\Phi \; \Rightarrow \; F \Psi \right)$ | 43.4~% |
| universality | safety | ${\sf G}\Phi$ | 19.8~% |
| absence | negated reachability | $G \neg \Phi$ | 7.4~% |
| precedence | liveness | $G\left(\neg \Phi W \Psi ight)$ | 4.5~% |
| absence | | $G((\Phi \land \neg \Psi \land F\Psi))$ | |
| | | $\Rightarrow (\neg \Phi' \cup \Psi))$ | 3.2~% |
| absence | safety | $G\left(\Psi \;\Rightarrow\; G \neg \Phi\right)$ | 2.1~% |
| existence | liveness | F∳ | 2.1~% |
| | | | $\approx 80 \%$ |
| Unless operator: | | | |
| $\phi W \psi == G\phi \vee \phi U\psi$ | | | |

Practical properties in LTL

- Reachability
 - Negated reachability
 - in tutti i cammini non riesco a raggiungere q (quindi q non e' mai raggiungibile)
 - Conditional reachability
 - Reachability (exists a path, as for home states)

not expressible posso solo dire che phi e' raggiungibile in tutte le esecuzioni

- Safety
 - Simple safety
 - Conditional safety
- Liveness

Fairness

G ¬ψ φUψ ∨ F φG (φ ⇒F ψ) and others

GF **\u0157** and others

F¬ψ

φυψ





- We want to find a correctness condition for a model to satisfy a specification.
- Language of a model: L(Model)
- Language of a specification: L(Spec).
- We need: $L(Model) \subseteq L(Spec)$.

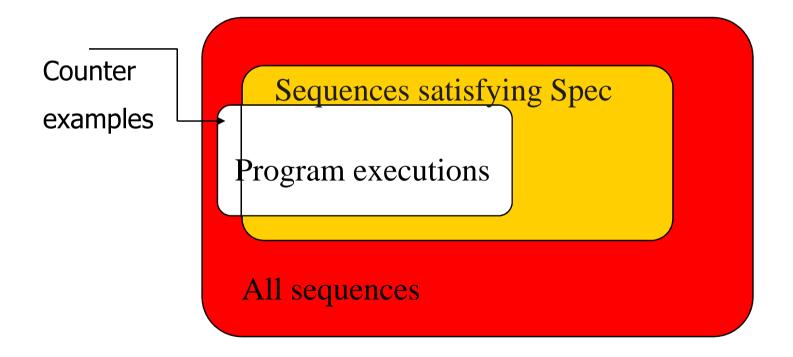


Sequences satisfying Spec

Program executions

All sequences





How to prove correctness?

- Show that $L(Model) \subseteq L(Spec)$.
- Equivalently: Show that L(Model) \cap L(Spec) = Ø.
- Model is specified as a Buchi automata, Spec can be specified as a Buchi automata *automatically translated* from LTL

Model checking schema

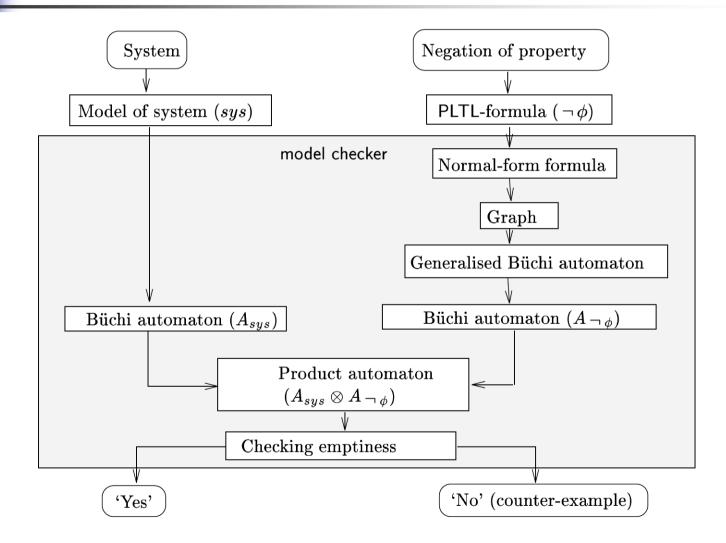
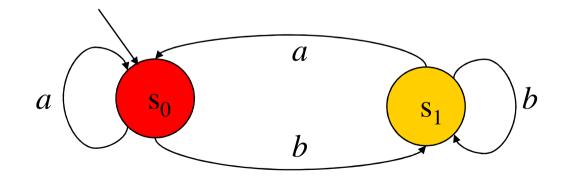


Figure 2.8: Overview of model-checking PLTL

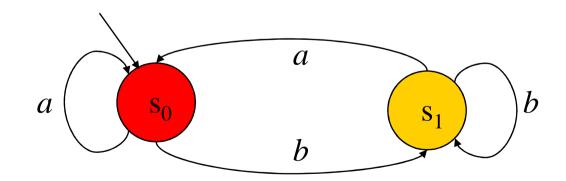
Automata over finite words

- A= $<\Sigma$, S, Δ , I, F>
- Σ (finite) the alphabet.
- S (finite) the states.
- $\Delta \subseteq S \times \Sigma \times S$ the transition relation.
- $I \subseteq S$ the starting states. (depicted with an incomig edge from nohere)
- $F \subseteq S$ the accepting states. *(depicted in red)*



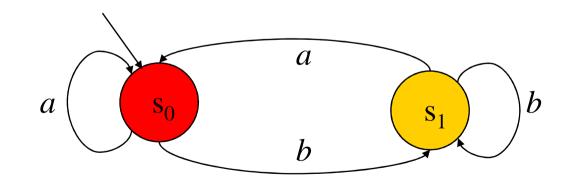
A *run* over a word

- A word over Σ , e.g., *abaab*.
- A sequence of states, e.g. s₀ s₀ s₁ s₀ s₀ s₁.
- Starts with an initial state.
- Follows the transition relation (s_i, C_i, s_{i+1}) .
- Accepting if ends at accepting state.



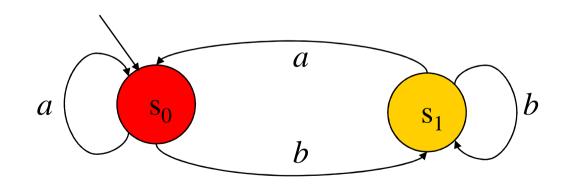
The language of an automaton

- The words that are accepted by the automaton.
- Includes *aabbba*, *abbbba*.
- Does not include *abab*, *abbb*.
- What is the language?



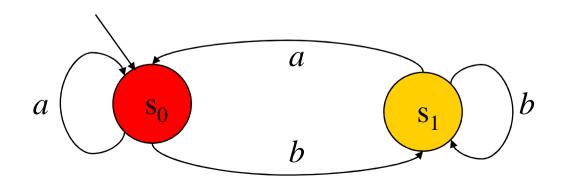
Automata over infinite words

- Similar definition.
- Runs on infinite words over Σ .
- Accepts when an accepting state occurs infinitely often in a run.



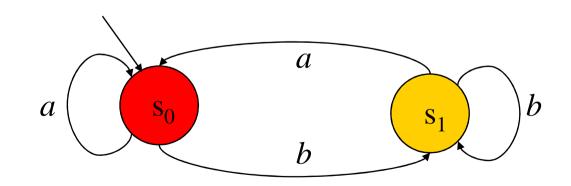
Automata over infinite words

- Consider the word *abababab*...
- There is a run $s_0s_0s_1s_0s_1s_0s_1...$
- For the word *bbbbb*... the run is s₀ s₁ s₁ s₁ s₁... and is not accepting.
- For the word aaabbbbb ..., the run is s₀ s₀ s₀ s₀ s₁ s₁ s₁ s₁
- What is the run for *ababbabbb* ...?



Specification using Automata

- Let each letter correspond to some propositional property.
- Example: a -- P0 enters critical section, b -- P0 does not enter section.



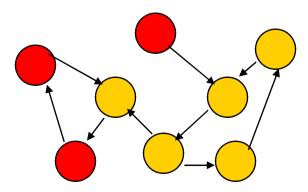
Generalized Büchi automata

- Acceptance condition F is a set
 F={f₁, f₂, ..., f_n} where each f_i is a set of states.
- To accept, a run needs to pass infinitely often through a state from every set f_i.



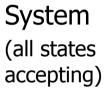
If there is an accepting run, then at least one accepting state repeats on it forever.

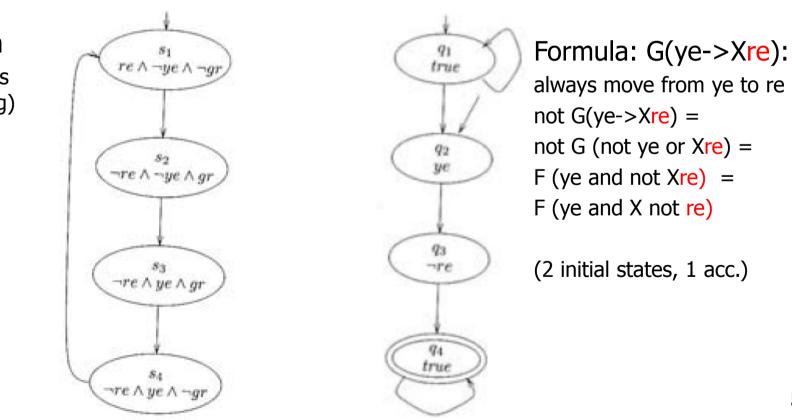
- Look at a suffix of this run where *all the states appear infinitely often*.
- These states form a strongly connected component on the automaton graph, including an accepting state.
- Find a component like that and form an accepting cycle including the accepting state.

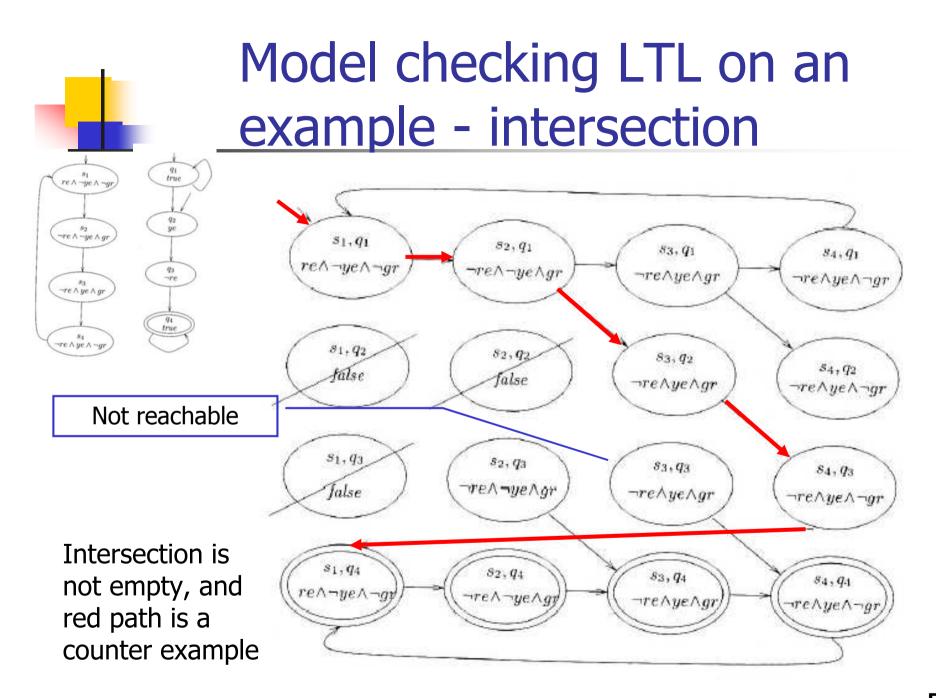


Model checking LTL on an example

Consider the traffic light example, we want to model check an LTL formula against the implementation of a traffic light specified as a Buchi automata. Also the formula is specified as a Buchi automata. These automata are as in the Peled's book, with proposition associated to states







Model checking LTL complexity (from JPK)

The automata of the formula φ has a size that depends on the number of subsets of the formula $O(2^{|\varphi|})$

The worst state space complexity of the product is $O(|Sys| * 2^{|\phi|})$, where Sys is the size of the system (number of nodes + number of transitions)

Checking emptiness is linear in number of states and transitions, and we finally get that:

The worst case time complexity of checking whether Sys satisfies the LTL formula φ is $O(|Sys| * 2^{|\varphi|})$



Fairness is used generically to refer to semantics contraints imposed on interleaved executions of concurrent systems.

E.g. P1 and P2, independent programs, that execute forever. On a real cpu they alternate into cpu, depending on the scheduler policy. We do not want to insert the scheduler policy in the model (too detailed), but we want to rule out interleaved executions that ignore enabled transitions of one process forever, since they do not correspond to any realistic scheduler.

Fair executions: motivations

Consider the following piece of code:

process Inc = while $\langle x \ge 0 \text{ do } x := x + 1 \rangle \text{ od}$ **process** Reset = x := -1

where $\langle .. \rangle$ means "atomic execution".

Does the program satisfies "F terminates"? No, since there is an execution in which only Inc is executed.

This situation is not possible if the OS schedule is fair, and we would like to rule-out from the model checking whose executions that are not fair



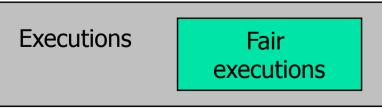
We want to consider only execution with fair behaviour.

Can be done:

• enforcing fairness in the formula: instead of verifying that the program satisfies ϕ , verify it satisfies *fair-constraint* $\Rightarrow \phi$ (GFInc.running \land GFReset.running) \Rightarrow F terminate

OR

• modifying the MC algorithm as to consider only fair executions



Some fairness definitions (JPK)

• Si tratta della definizione della parte di fairness constraint in *fair-constraint* $\Rightarrow \phi$

- Vogliamo che il fair constraint sia abbastanza ampio (nel senso che deve essere soddisfatto in molte esecuzioni).
- Esempi di casi limite per la determinazione delle esecuzioni fair in una proprieta' di terminazione, del tipo

fair-constraint \Rightarrow F terminate

- *fair-constraint* = true : il programma deve terminare su tutte le esecuzioni
- fair-constraint = false: anche se il programma non termina la proprieta' e' soddisfatta

Some definitions (JPK) for *fairness-constraint*

- Unconditional fairness: Un unconditional fairness constraint is an LTL formula of the form: $GF \psi$ also stated as true $\Rightarrow GF \psi$
- Weak fairness (justice): A weak fairness constraint is an LTL formula of the form: FG $\varphi \Rightarrow$ GF ψ as in: FG enabled(a) \Rightarrow GF executed(a)
 For considering only paths in which, from a certain point on, if you keep asking, you get it infinitely often
 A strong fairness constraint is an LTL formula of the form: GF $\varphi \Rightarrow$ GF ψ

For considering only paths in which, if you ask infinitely often, you get it infinitely often

Som Usually unconditional and strong are useful for solving contentions, and weak is often sufficient to resolve non-determinism due to interleaving semantics

- Unconditional fairness: Un unconditional fairness constraint is an LTL formula of the form: GF ψ also stated as true \Rightarrow GF ψ
- Weak fairness (justice): A weak fairness constraint is an LTL formula of the form: FG $\varphi \Rightarrow$ GF ψ

as in: FG enabled(a) \Rightarrow GF executed(a)

• Strong fairness: A strong fairness constraint is an LTL formula of the form: GF $\phi \Rightarrow$ GF ψ

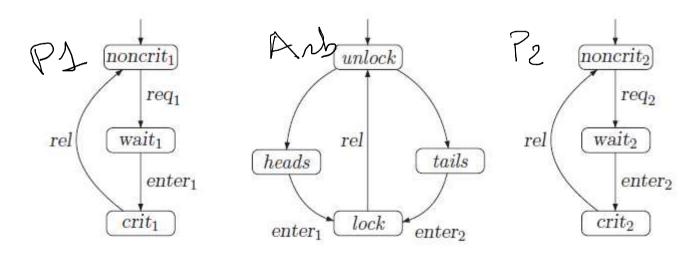
Sono ``fairness assumptions" gli AND di fairness constraints

Esempio di fairness

Sys = P1 || P2 || Arb su {enter1 e enter2}

Sys |= GF crit1 ?? Questo si traduce in $\forall \sigma \in \text{Lang}(\text{Sys}), \text{ s } \mid = \text{GF crit1}$

La formula LTL è falsa perché esiste un'esecuzione in cui Arb sceglie sempre tail

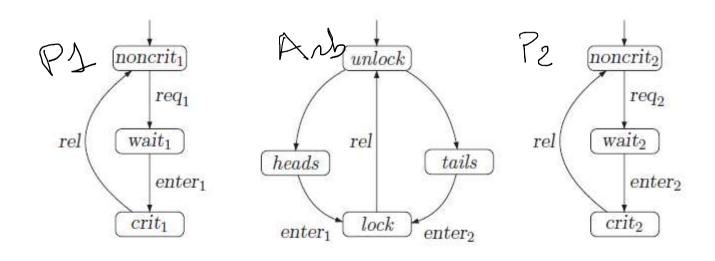


Esempio di fairness

Sys = P1 || P2 || Arb su {enter1 e enter2} Fairness constraint (unconditional): GF heads AND GF tail

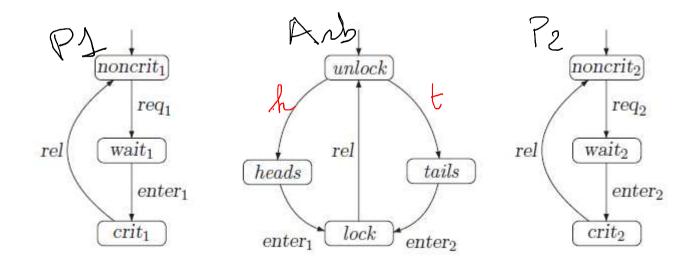
ed è vero che:

Sys |= (GF heads AND GF tail) --> GF crit1



Action-based vs. transition based fairness

Di fatto nell'esempio precedente la condizione di fairnes è espressa sugli stati locali (heads, tails) ma forse sarebbe più naturale esprimerla sulle azioni h e t che esprimono la scelta non deterministica. È possibile provare che si può tradurre la specifica action-base in specifica state-based (modificando gli stati del sistema)



If MC is so good, why deductive verification methods exists?

- Model checking works only for finite state systems. Would not work with
 - Unconstrained integers.
 - Unbounded message queues.
 - General data structures:
 - queues
 - trees
 - stacks
 - parametric algorithms and systems.

The state space explosion

- Need to represent the state space of a program in the computer memory.
 - Each state can be as big as the entire memory!
 - Many states:
 - Each integer variable has 2^32 possibilities.
 Two such variables have 2^64 possibilities.
 - In concurrent protocols, the number of states usually grows exponentially with the number of processes.

If MC is so constrained, is it of any use?

- Many protocols are finite state.
- Many programs or procedure are finite state in nature. Can use abstraction techniques.
- Sometimes it is possible to decompose a program, and prove part of it by model checking and part by theorem proving.
- Many techniques to reduce the state space explosion.